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ey Docket No. 09792909-5305

FABRICATION METHOD

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shibue, H.

Application No. 10/052,078

Filed: January 17, 2002

For: SEMICONDUCTOR DEVICE AND

In re Patent Application of:

(Compart Unit: 2824

Examiner: Smith, Bradley

I hereby certify that this document is being deposited with the United States Postal Service as first class mail in an envelope addressed to: MAIL STOP ISSUE FEE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 20, 2004

For: SEMICONDUCTOR DEVICE AND

MAIL STOP ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

THEREOF

Attn: Official Draftsman

SUBMISSION OF FORMAL DRAWINGS IN RESPONSE TO THE NOTICE OF ALLOWABILITY

Dear Sir:

Applicants hereby submit one (1) sheet of corrected formal drawings (Figs. 3 - 4 - 5), including the drawing amendment as approved by the Examiner July 1, 2004 in the above-identified patent application.

Applicants submit that no new matter is added by the formal drawings.

This submission is on or before the three-month period of response set in the Notice of Allowance.

The Commissioner is hereby authorized to charge any additional fees which may be required, or to credit any overpayment to account no. 19-3140. A duplicate of this sheet is enclosed.

Respectfully submitted,

Dated: August 20 2004 By:

David R. Metzger

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